THE COMMISSIONER OF PATENTS AND TRADEMARKS WASHINGTON, D.C. 20231

Sir:

Transmitted herewith for filing is the Utility patent application of:

INVENTOR: MOTOTSUGU FUJII, OSAMU TADA, KAZUNOBU MORIMOTO, AKIRA YAMAGIWA AND HISASHI

NANAO

FOR: LOGIC VERIFICATION SYSTEM

Enclosed are:

- □ 21 sheets of □ formal □ informal drawings.
- ☑ Request for Priority to claim priority date of October 17, 2002.
- ☑ An Executed Declaration.
- ☑ An assignment executed by inventors to Renesas Technology Corp.
- ☑ An Information Disclosure Statement
- ☑ The filing fee is calculated as shown below:

For	Number Filed		Number Extra	RATE	CALCULATIONS
TOTAL CLAIMS	7	-20	0	x\$18 =	0.00
INDEPENDENT CLAIMS	2	-3	0	x\$86=	0.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable) +\$290 =					
. BASIC FEE					\$770.00
TOTAL OF ABOVE					770.00
REDUCTION BY ½ FOR FI		,	e 37 C.F.R. §§ 1.9, 1.27, 1.28 CHED	3).	
				TOTAL	770.00

- A check in the amount of \$810.00 to cover the filing fee and assignment fee is enclosed. Ø
- \boxtimes The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Number 08-1480.

Respectfully submitted,

Stanley P. Fisher

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uan Carlos A Marquez

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October 9, 2003